



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
YAMAMOTO et al. )  
Application Number: 10/817,004 )  
Filed: April 5, 2004 )  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE )  
AND MANUFACTURING METHOD THEREOF )  
ATTORNEY DOCKET NO. HITA.0536 )

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

In response to the notice of omitted items, the Applicants wish to correct minor typographical errors as follows: